
 <p>INFORMATION DISCLOSURE CITATION</p> <p>PTO-1449</p>				<i>Complete If Known</i>	
				Application Number	10/625,914
				Filing Date	July 22, 2003
				First Named Inventor	Ely K. Tsern et al.
				Art Unit	2818
				Examiner Name	Lam, David
1	of	2	Attorney Docket Number	60809-0137	

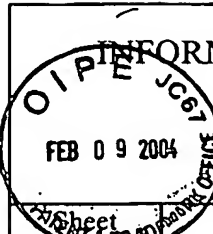
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Examiner Signature		Date Considered	4/04
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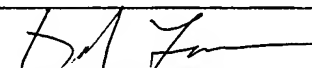
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FOREIGN PATENT DOCUMENTS							
Examiner Initials	Foreign Patent Document Country Code <sup>2</sup> - Number <sup>3</sup> - Kind Code <sup>4</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Translation	
						Yes	No
DL	JP 08077769	03/22/1996	Patent Abstracts of Japan	11	406	AKS	<input checked="" type="checkbox"/>

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS							
Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published						
DL	Choi et al., "Battery Operated 16M DRAM with Post Package Programmable and Variable Self Refresh", <i>IEEE, Symposium on VLSI Circuits Digest of Technical Papers</i> , pp. 83-84 (1994)						
	Kagenishi, "Low Power Self Refresh Mode DRAM with Temperature Detecting Circuit", <i>IEEE, Symposium on VLSI Circuits Digest of Technical Papers</i> , pp. 43-44 (1993)						
	Yamauchi et al., "A Circuit Technology for a Self-Refresh 16Mb DRAM with Less than 0.5 $\mu$ A/MB Data-Retention Current", <i>IEEE J. of Solid-State Circuits</i> , 30(11):1174-1182 (1995)						
	Yoo et al., "A 256M DRAM With Simplified Register Control for Low Power Self Refresh and Rapid Burn-In", <i>IEEE, Symposium on VLSI Circuits Digest of Technical Papers</i> , pp. 85-86 (1994)						
DL	Yoo et al., "Variable Vcc Design Techniques for Battery Operated DRAMs", <i>IEEE, Symposium on VLSI Circuits Digest of Technical Papers</i> , pp. 110-111 (1992)						

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